



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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Patent Application of:

Hisashi OHTANI et al.

Serial No. 09/837,552

Filed: 04/19/2001

For: SEMICONDUCTOR DEVICE AND

MANUFACTURING METHOD THEREOF

Examiner: N.D. Richards

Group Art Unit: 2815

TECHNOLOGY CENTER 2800

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CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as First Class Mail in an envelope addressed to: Commissioner for Patents, Washington, DC 20231, on 10/11/02

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
Washington, DC 20231

Sir:

In accordance with the duty of disclosure as set forth in 37 C.F.R. §1.56, Applicants hereby submit the following information in conformance with 37 C.F.R. §§ 1.97 and 1.98. Pursuant to 37 C.F.R. § 1.98, a copy of each of the documents cited is enclosed.

The enclosed references were cited by the Examiner in related application Serial No. 09/837,877 in a USPTO office action dated July 31, 2002. JP 10-374878 listed on the Form PTO-892 is not included since it was previously cited in this application.

It is requested that the accompanying information disclosure statement be considered and made of record in the above-captioned application. To assist the Examiner, the documents are listed on the attached form PTO-1449. It is respectfully requested that an Examiner initialed copy of this form be returned to the undersigned.

The Commissioner is hereby authorized to charge any fees connected with this filing which may be required now, or credit any overpayment to Deposit Account No. 19-2380.

Respectfully submitted,

By:

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